

AMENDMENTS TO THE SPECIFICATION

The title is changed as follows:

~~AN IMPROVED SURGE ARRESTOR BASED ON ELECTRICAL VARISTORS~~
METHOD OF MANUFACTURING SURGE ARRESTOR

Page 1, first paragraph:

This is a continuation of U.S. Application No. 09/869,097 filed ~~June 22, 2001~~ August 29, 2001 (now abandoned), which is a National Stage Entry under 35 U.S.C. § 371 of International Application No. PCT/FR00/02930, filed on October 20, 2000, the entire disclosure of which is incorporated herein by reference.

Page 1, after the title, the following heading is inserted:

BACKGROUND OF THE INVENTION

Page 2, first full paragraph:

Numerous types of ~~arrestor~~ arrestors have already been proposed.

Page 7, before the first full paragraph, the following heading is inserted:

SUMMARY OF THE INVENTION

Page 7, fifth paragraph:

Other characteristics, objects, and advantages of the present invention will appear on reading the following detailed description together with the accompanying drawings, given as non-limiting examples ~~and in which,~~

Page 7, after the fifth full paragraph, the following heading is inserted:**BRIEF DESCRIPTION OF THE DRAWINGS****Page 7, sixth full paragraph:**

~~accompanying~~ Accompanying Figures 1 to 4 are diagrams showing various successive steps in the manufacture of a surge arrestor constituting a preferred implementation of the present invention.

Page 7, after the sixth full paragraph, the following title is inserted:**DETAILED DESCRIPTION OF ILLUSTRATIVE NON-LIMITING EMBODIMENTS**

Please delete the present Abstract of the Disclosure and replace it with the following new Abstract of the Disclosure.

The present invention relates to a method of manufacturing surge arrestors, the method ~~being of the type comprising the steps which consist in:~~ including [[.]] stacking varistors [[(10)]]; and ~~between the steps of stacking and forming~~ [[the]] a coating of composite material [[(40),]] on the stack. Between these steps, also included is placing a bead [[(30)]] of flexible, adhesive, and dielectric material on the previously formed stack in register with the various interfaces between each pair of adjacent varistors [[(10)]]].